



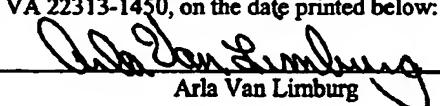
IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

APPLICANT(S): Wilson et al
SERIAL NO.: 10/044,394
FILING DATE: January 11, 2002
TITLE: Managing Latencies in Accessing Memory of Computer System
EXAMINER: Thang H. Ho
CONFIRMATION NO.: 9298
ART UNIT: 2188
ATTY. DKT. NO: PDNO 10012382-1

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: Oct. 16, 2004


Arla Van Limburg

AMENDMENT

C. Barnes
3/31/05
COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

Sir:

In response to the Final Office Action mailed October 19, 2004, please amend the above-identified application as follows: